

**Sealed Choke Coil SDER031B type**

■ **Features**

Low profile : 3.0mm x 3.0mm x 1.2mm

Low coil resistance with large currents.

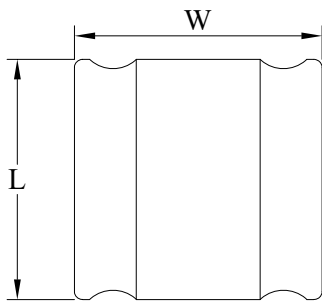
High magnetic shield construction should actualize high resolution for EMC protection.

100% lead (Pb) free meet RoHS standard

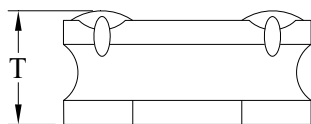
■ **Application**

Cellular phones, LCD displays, HDDs, DVCs, DSCs, PDAs etc..

■ **Outline Dimensions**



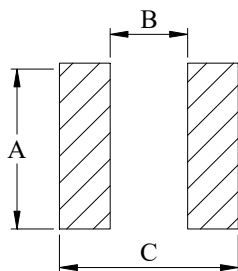
Code	Dimensions (mm)
L	3.0 ± 0.2
W	3.0 ± 0.2
T	1.2 Max.



Note: This graph is in regard to outline dimensions spec. For outer appearance, please refer to actual product.

■ **Recommend Land Pattern Dimensions**

The customer shall determine the land dimensions shown below after confirming and safety.



A	2.7
B	1.4
C	3.1

Unit : mm

■ Specifications

Part Number	L0 Inductance ( $\mu\text{H}$ ) @ (0A)	$R_{dc}$ ( $\text{m}\Omega$ )		Heat Rating Current DC Amps. $I_{dc}$ ( A )		Saturation Current DC Amps. $I_{sat}$ ( A )	
		Typical	Maximum	Typical	Maximum	Typical	Maximum
SDER031B-1R0MS	1.0	52	62	2.50	2.25	3.35	3.00
SDER031B-1R5MS	1.5	60	72	2.30	2.10	3.10	2.60
SDER031B-2R2MS	2.2	84	101	2.25	2.00	2.90	2.40
SDER031B-3R3MS	3.3	134	161	1.71	1.53	1.92	1.72
SDER031B-4R7MS	4.7	184	221	1.43	1.30	1.71	1.53
SDER031B-6R8MS	6.8	256	307	1.25	1.13	1.49	1.24
SDER031B-100MS	10.0	397	496	1.00	0.90	1.26	1.05
SDER031B-150MS	15.0	572	686	0.80	0.72	1.10	0.83
SDER031B-220MS	22.0	850	1020	0.65	0.60	0.86	0.72
SDER031B-330MS	33.0	1387	1733	0.50	0.45	0.65	0.58
SDER031B-470MS	47.0	1908	2385	0.44	0.39	0.50	0.45

\* : If you require another part number please contact with us.

\*\* : Inductance Tolerance  $\pm 20\%$

Note 1. : All test data is referenced to  $25^{\circ}\text{C}$  ambient.

Note 2. : Test Condition:1MHz, 1.0Vrms

Note 3. :  $I_{dc}$  : DC current (A) that will cause an approximate  $\Delta T$  of  $40^{\circ}\text{C}$

Note 4. :  $I_{sat}$  : DC current (A) that will cause L0 to drop approximately 30%

Note 5. : Operating Temperature Range  $-55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$

Note 6. : The part temperature (ambient + temp rise) should not exceed  $125^{\circ}\text{C}$  under the worst case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

Note 7. : The rated current as listed is either the saturation current or the heating current depending on which value is lower.

### Current Characteristic

