

1/32W, 01005, Thick Film Chip Resistor (Halogen Free)

Reversion History:

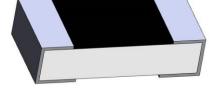
Date	Revision	Changes
2009.02.25	A0	New Approval
2011.09.27	A1	
2012.07.03	A2	
2014.05.21	А3	
2019.09.23	A4	Modify format
2021.01.11	A5	Modify lead free related description



1/32W, 01005, Thick Film Chip Resistor (Halogen Free)

Features / Applications :

- Telecommunication Equipment, Digital Cameras, Watches, Pocket Calculators, Computers, Instruments.
- Halogen Free Epoxy
- RoHS compliant
 - Glass/electrode of resistor with lead free meet RoHS requirements
 - Pb contained in resistive element is exempted by RoHS



Electrical Specifications:

Power Rating*	Resistance Values Series	Resistance Tolerance	Resistance Range (Ω)	Temperature Coefficient of Resistance ppm /°C (Code)	Operating Max. Temperature Operatin Range Voltage*	
	F24 comics 8	± 1.0% (F)	1.0~9.76	-200~+600		15V
1/32W -	E24 series & E96 series		10~91	± 300		
	250 301103		100∼1.62M	± 200	-55℃ to 125℃	
	E24 series	± 5.0% (J)	1.0~9.1	-200~+600	-55 C to 125 C	
			10~91	± 300		
			100∼10M	± 200		
Jumper	Resistance		Rated current		Operating Tem Range	
Jumper	Below 50mΩ		0.5A		-55℃ to 125℃	

Note: *Package Power Temperature Derating Curve

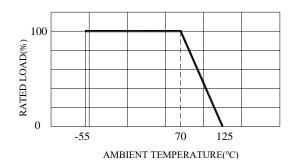


Figure 1. : Power Temperature Derating Curve

Note: **Resistors shall have a rated DC or AC(rms.) continuous operating voltage corresponding to the power rating, as calculated from the following formula

$$V = \sqrt{P imes R}$$
 Where V : Rated voltage (V)

P : Rated power (W)

R : Nominal resistance (Ω)

the voltage so obtained exceeds the maximum operating voltage, this maximum voltage shall be the rated voltage.

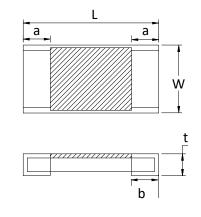
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DOCUMENT : RQ-NH



Outline Drawing:

Dimensions



Code Letter	Dimension		
L	0.40 ± 0.02		
W	0.20 ± 0.02		
t	0.13 ± 0.02		
а	0.10 ± 0.03		
b	0.10 ± 0.03		

Unit: mm

Note: *Which resistance layer(protection coating layer) is down : just $\pm 1\%$ (F) for $1.0\Omega \simeq 9.76\Omega$

** No marking on the protect coating

Type Designation:

RR0204S - XXXX - X NH

(1) (2) (3) (4)

Note:

(1) Series No.

(2) Resistance value : $103 = 10k\Omega$ (E24) ; $1131 = 1.13k\Omega$ (E96)

(3) Tolerance : $F = \pm 1\%$, $G = \pm 2\%$, $J = \pm 5\%$, $X = Jumper (Below 50m\Omega)$

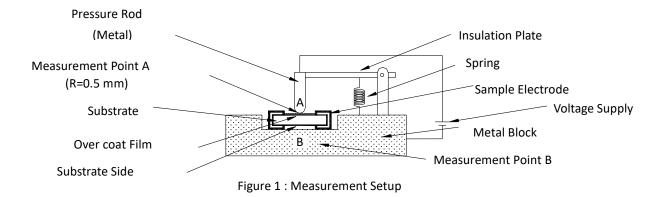
(4) NH= Sn plating (Lead free / Halogen free)



Characteristics:

Electrical

lt ave	Specification and Requirement			Test Method
Item	Resistor	Jumper		(Refer to JIS C 5201)
Short Time Overload	△R: ±(2%+ 0.1Ω)	Max. $50 m\Omega$	(1)	Applied voltage:
	Without damage by			2.5 x rated voltage or
	flashover, spark, arcing,			2 x maximum operating voltage
	burning or breakdown			whichever is less
			(2)	Test time : 5 seconds
Insulation Resistance	Over 100 M Ω on Overcoat layer face up		(1)	Setup as figure 1
	Over 1,000 M Ω on Substrate side face up		(2)	Test voltage: 100VDC
			(3)	Test time:
				60 + 10 / -0 seconds
Voltage Proof	△R: ±(2%+ 0.1Ω)	Max. $50 m\Omega$	(1)	Setup as figure 1
	Without damage by		(2)	Test voltage: 50VAC(rms.)
	flashover, spark, arcing,		(3)	Test time:
	burning or breakdown			60 +10 / -0 seconds







Mechanical

lh a sa	Specification and Requi	rement	Took Mark ad (US 5201)		
Item	Resistor	Jumper	Test Method (JIS 5201)		
Solderability	The surface of terminal immersed shall be		Solder bath:		
	minimum of 95% covered with a new coating		After immersing in flux, dip in		
	of solder		245 ± 5°C molten solder bath for		
			2 ± 0.5 seconds		
Resistance to Solder	ΔR: ±(1%+ 0.05Ω)	Max. $50 m\Omega$	(1) Immersed at solder bath of		
Heat	Without distinct deformation		270 ± 5 °C for 10 ± 1 seconds		
	in appearance		(2) Measuring resistance		
			1 hour after test		
	ΔR: ±(0.5%+ 0.05Ω)		(1) Vibration frequency:		
Vibration	Without mechanical damage su	ch as break	10Hz to 55Hz in 60 seconds as a		
			period		
			(2) Vibration time: period cycled for 2		
			hours in each of 3 mutual		
			perpendicular direction total.		
			(3) Amplitude: 1.5mm		
	△R: ±(0.25%+ 0.05Ω)		(1) Peak value: 490N		
Shock	Without mechanical damage su	ch as break	(2) Duration of pulse: 11ms		
			(3) 3 times in each positive and		
			negative direction of 3 mutual		
			perpendicular directions		
	△R: ±(1%+ 0.05Ω)		Bending value : 3mm for 30 \pm 1 seconds		
Bending Test	Without mechanical damage such as break				
	Without mechanical and distinc	t damage in	(1) Solvent:		
Solvent Resistance	appearance		Trichloroethane or Isopropyl alcohol		
			(2) Immersed in solvent at		
			room temperature for 90 seconds		



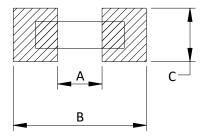


Endurance

	Specification and Requirement			T-+ M-+h - I (UC 5204)		
Item	Resistor Jumper			Test Method (JIS 5201)		
Rapid change of	△R: ±(1%+ 0.05Ω)	Max. $50 m\Omega$	(1)	Repeat 5 cycle as follow:		
Temperature	Without distinct damage in			(-55 ± 3°C,30minutes)		
	appearance			→(Room temperature, 2~3 minutes)		
				\rightarrow (+125 ± 2°C,30minutes) \rightarrow (Room		
				temperature, 2~3 minutes)		
			(2)	Measuring resistance		
				1 hour after test		
Moisture with Load	△R: ±(5%+ 0.1Ω)	Max. 50m $Ω$	(1)	Environment condition:		
	Without distinct damage in			40 ± 2°C,90~95% RH		
	appearance		(2)	Applied Voltage: rated voltage		
	Marking should be legible		(3)	Test period: (1.5 hour ON)		
				→ (0.5 hour OFF) cycled for total		
				1,000 + 48 / - 0 hours		
			(4)	Measuring resistance		
				1 hour after test		
Load Life	△R: ±(5%+ 0.1Ω)	Max. 100m $Ω$	(1)	Test temperature: 70 ± 2°C		
	Without distinct damage in		(2)	Applied Voltage: rated Voltage		
	appearance		(3)	Test period: (1.5 hour ON)		
				→ (0.5 hour OFF) cycled for total		
				1,000 + 48 / - 0 hours		
			(4)	Measuring resistance		
				1 hour after test		
Low Temperature	△R: ±(5%+ 0.1Ω)	Max. 100m $Ω$	(1)	Store temperature: -55 \pm 3 $^{\circ}$ C		
Store	Without distinct damage in			for total 1,000 + 48 / - 0 hours		
	appearance		(2)	Measuring resistance		
				1 hour after test		
High Temperature	△R: ±(5%+ 0.1Ω)	Max. 100m $Ω$	(1)	Store temperature: -125 ± 2°C		
Store	Without distinct damage in			for total 1,000 + 48 / - 0 hours		
	appearance		(2)	Measuring resistance		
				1 hour after test		



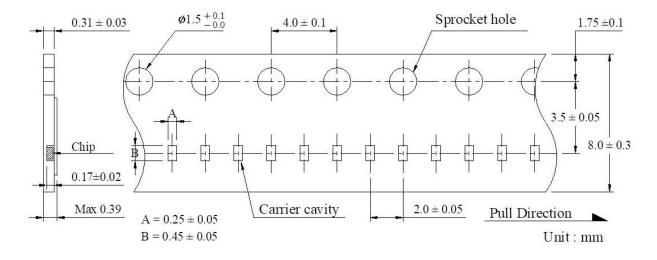
Recommend Land Pattern Dimensions:



А	0.15 ~ 0.20		
В	0.5 ~ 0.8		
С	0.2 ~0.4		

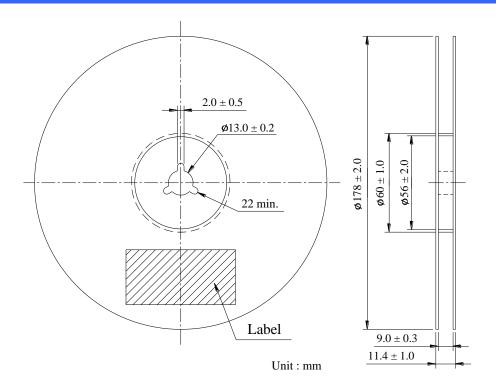
Unit: mm

TAPE PACKAGING DIMENSIONS:





REEL DIMENSIONS:



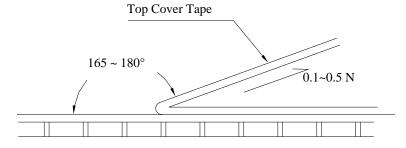
Numbers of Taping: 20,000 pieces/reel

The following items shall be marked on the reel.

- (1) Type designation.
- (2) Quantity
- (3) Manufacturing date code
- (4) Manufacturer's name

Peel force of top cover tape

The peel speed shall be about 300 mm/min. The peel force of top cover tape shall be between 0.1 to 0.5 N.



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Care Note:

Care note for storage

- (1) Chip resistor shall be stored in a room where temperature and humidity must be controlled. (temperature 5 to 35°C, humidity 45 to 85% RH) However, a humidity keep it low, as it is possible.
- (2) Chip resistor shall be stored as direct sunshine doesn't hit on it.
- (3) Chip resistor shall be stored with no moisture, dust, a material that will make solderability inferior, and a harmful gas (Hydrogen chloride, sulfurous acid gas, and Hydrogen sulfide)

Care note for operating and handling

- (1) It is necessary to protect the edge and protection coat of resistors from mechanical stress.
- (2) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (3) Resistors shall be used with in rated range shown in specification. Especially, if voltage more than specified value will be loaded to resistor, there is a case it will make damage for machine because of temperature rise depending on generating of heat, and increase resistance value or breaks.
- (4) In case that resistor is loaded a rated voltage, it is necessary to confirms temperature of a resistor and to reduce a load power according to load reduction curve, because a temperature rise of a resistor depends on influence of heat from mounting density and neighboring element.
- (5) Observe Limiting element voltage and maximum overload voltage specified in each specification.
- (6) If there is possibility that a large voltage (pulse voltage, shock voltage) charge to resistor, it is necessary that operating condition shall be set up before use.

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